

FIG. 1

200

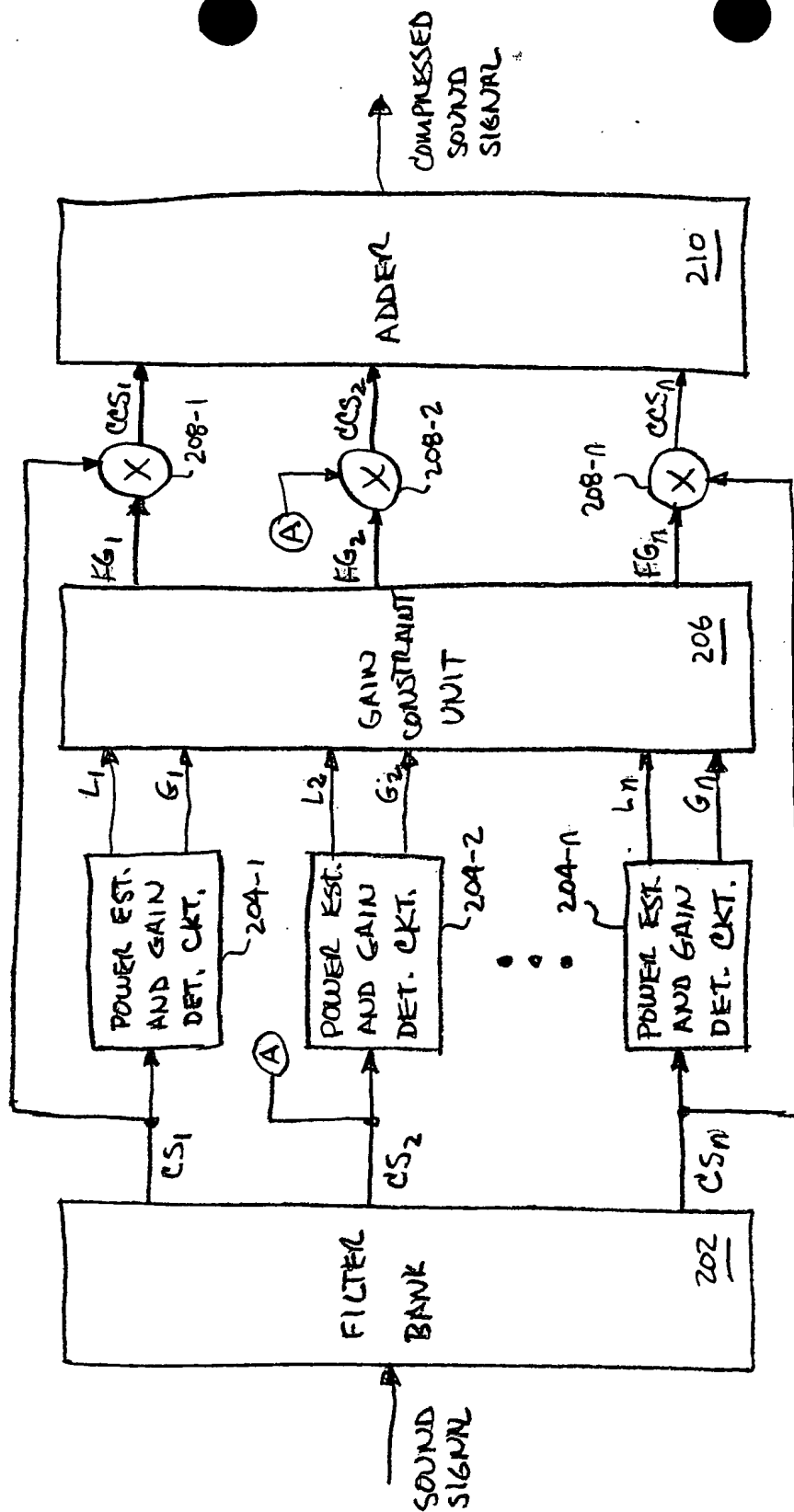


FIG. 2

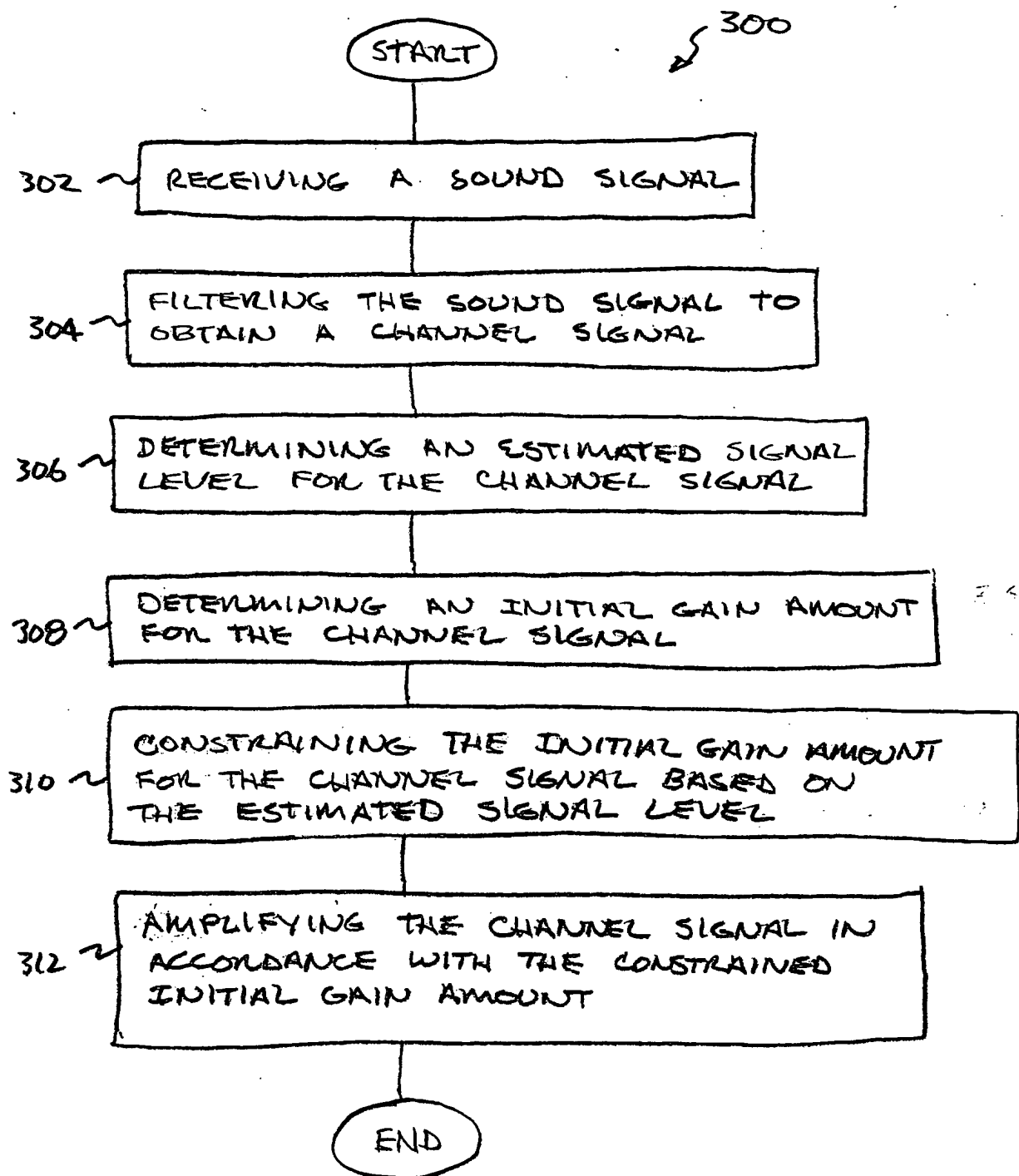


FIG. 3

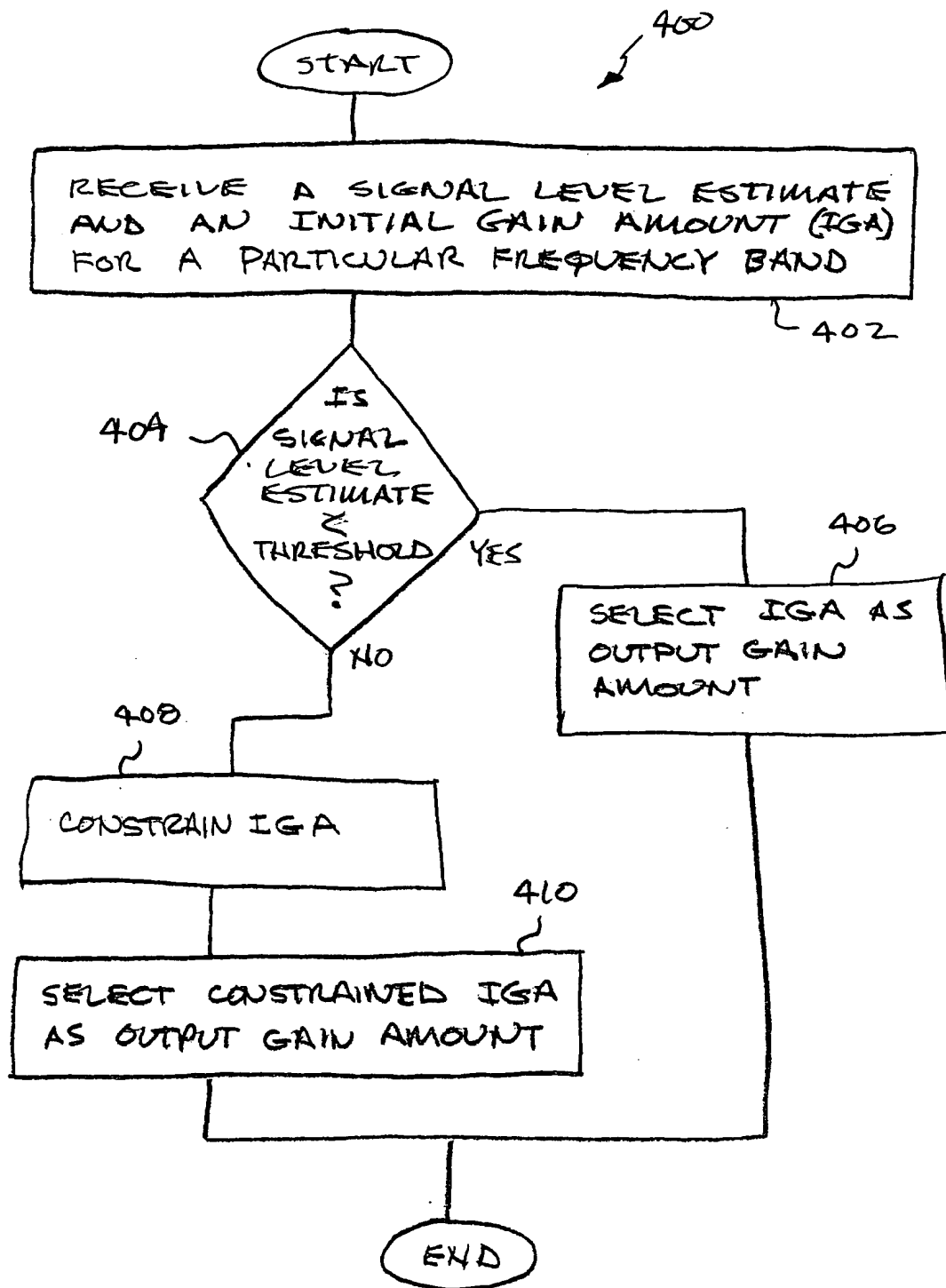
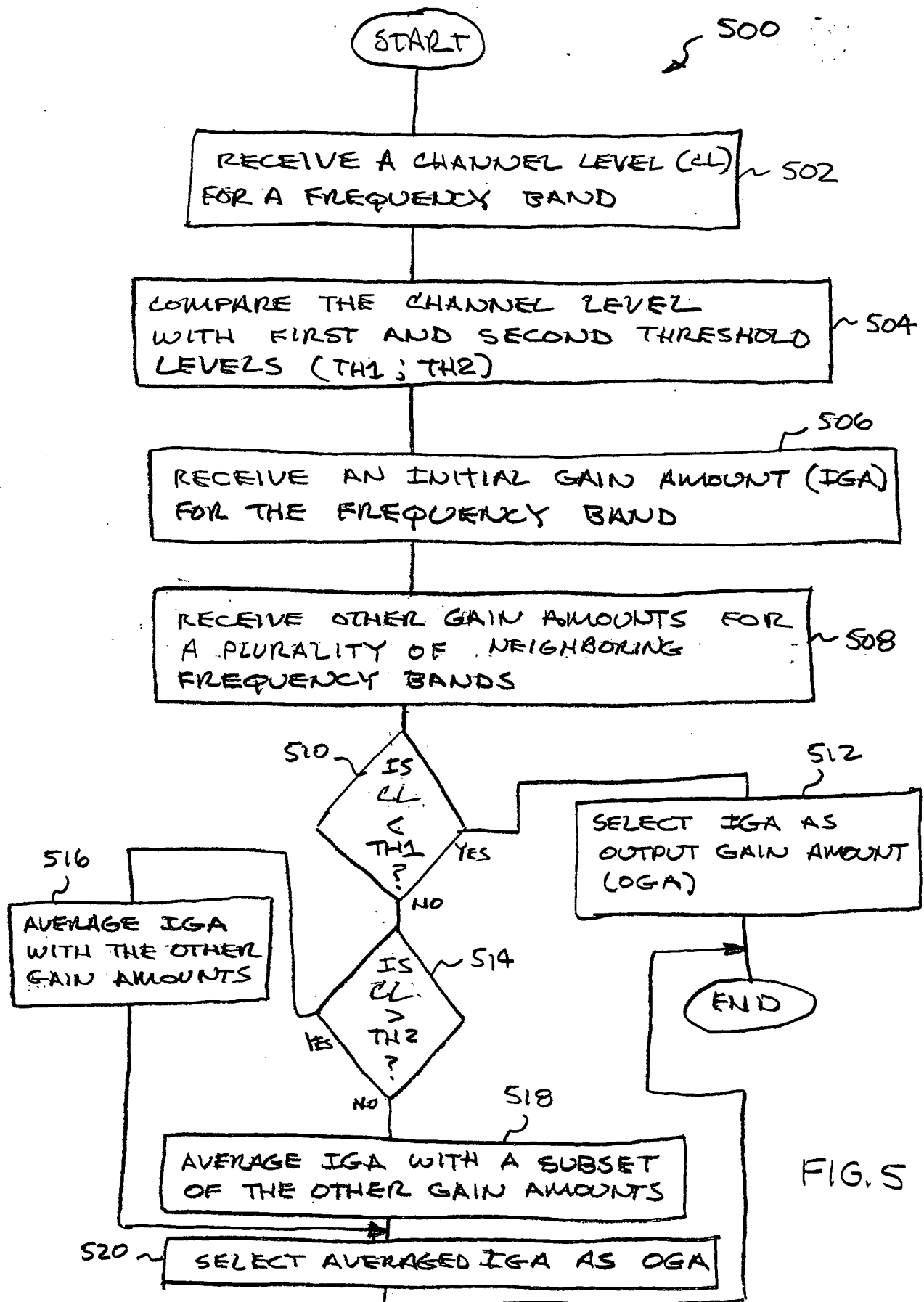


FIG. 4



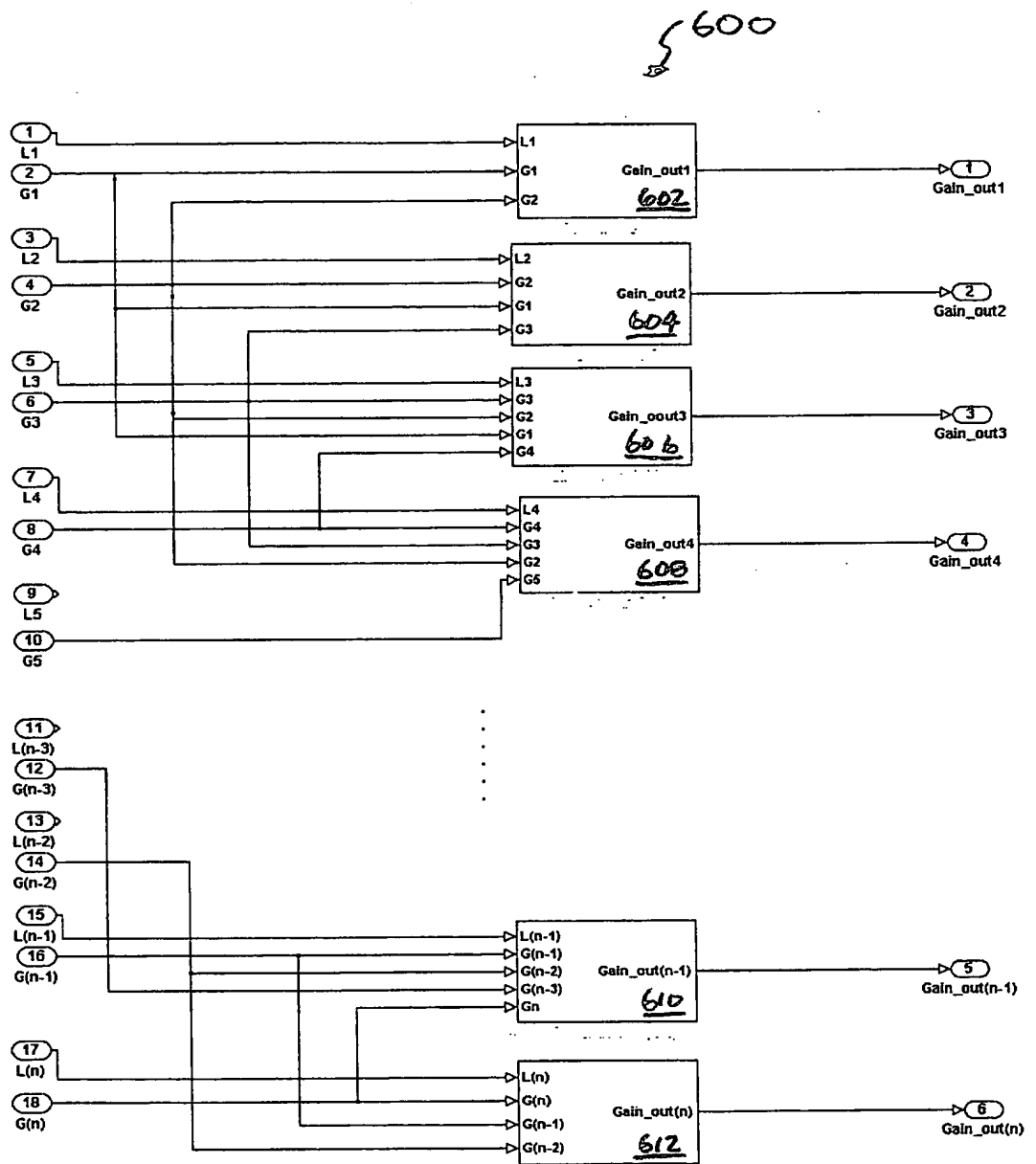


FIG. 6

FIG. 7 is a block diagram of a system 700 for processing input signals L1, G1, and G2. The system includes two Relational Operators (702, 704), two Summation Circuits (710, 714), two Gain Circuits (712, 716), and a Multipoint Switch (708). Input L1 is connected to Relational Operator 702 and Summation Circuit 710. Input G1 is connected to Relational Operator 704 and Summation Circuit 710. Input G2 is connected to Summation Circuit 714. A constant value of 0 is connected to Summation Circuit 710. The outputs of Relational Operators 702 and 704 are connected to Summation Circuit 710. The outputs of Summation Circuits 710 and 714 are connected to Gain Circuits 712 and 716, respectively. The outputs of Gain Circuits 712 and 716 are connected to the Multipoint Switch 708. The Multipoint Switch 708 has four inputs and one output, Gain\_out1. The output of the Multipoint Switch 708 is connected to Gain\_out1.

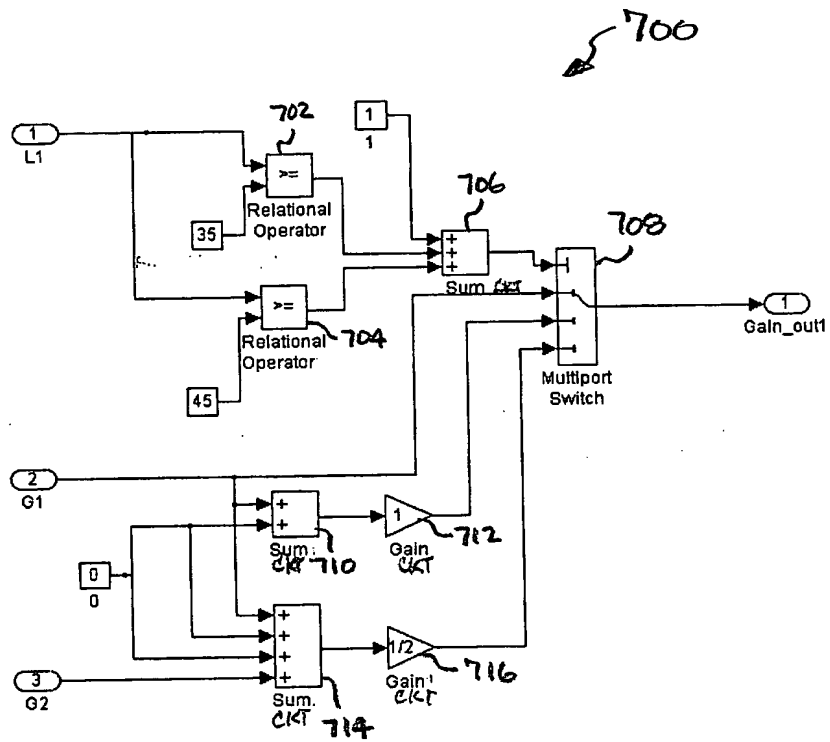


FIG. 7

FIG. 8 is a block diagram of a system 800. The system 800 includes a first input 1, a second input 2, a third input 3, and a fourth input 4. The first input 1 is connected to a first summing junction 702 and a first relational operator 704. The second input 2 is connected to a second summing junction 710. The third input 3 is connected to a third summing junction 714. The fourth input 4 is connected to a fourth summing junction 716. The first relational operator 704 is connected to the first summing junction 702 and the second summing junction 710. The second summing junction 710 is connected to a first gain block 712. The third summing junction 714 is connected to a second gain block 716. The first gain block 712 and the second gain block 716 are connected to a multiplex switch 708. The multiplex switch 708 is connected to an output 1, which is labeled Gain\_out2.

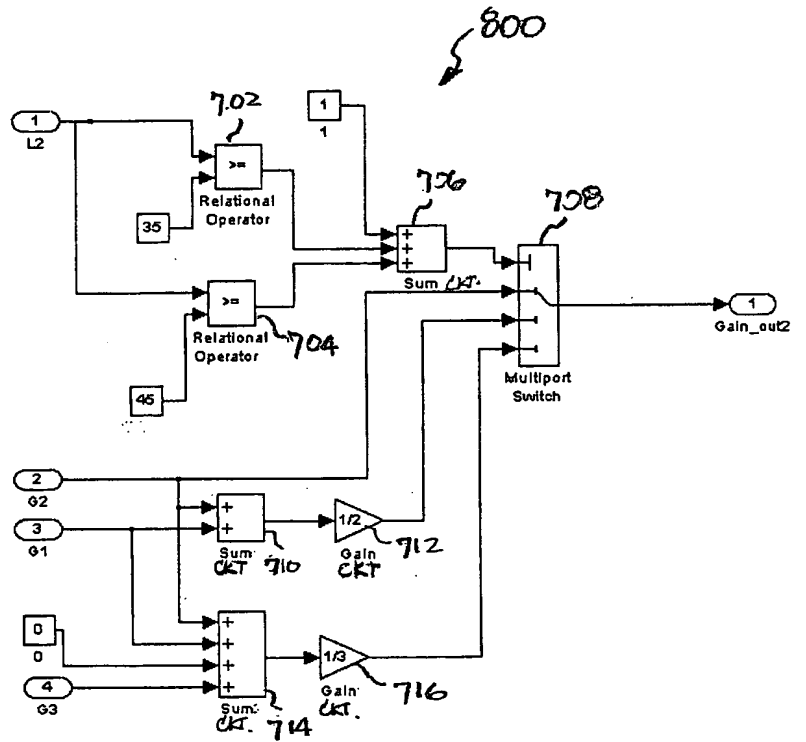


FIG. 8



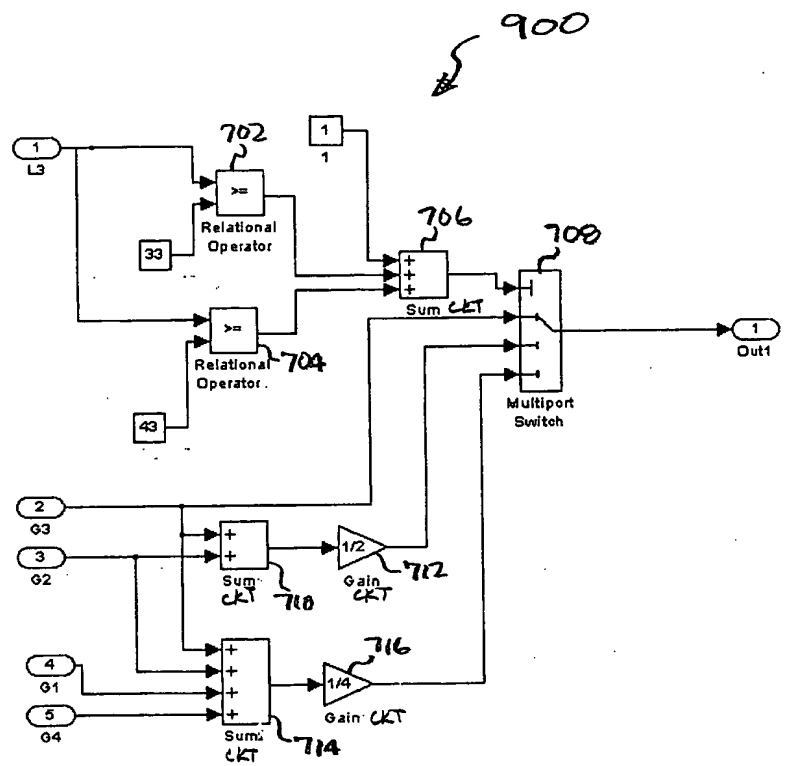


FIG. 9

1000

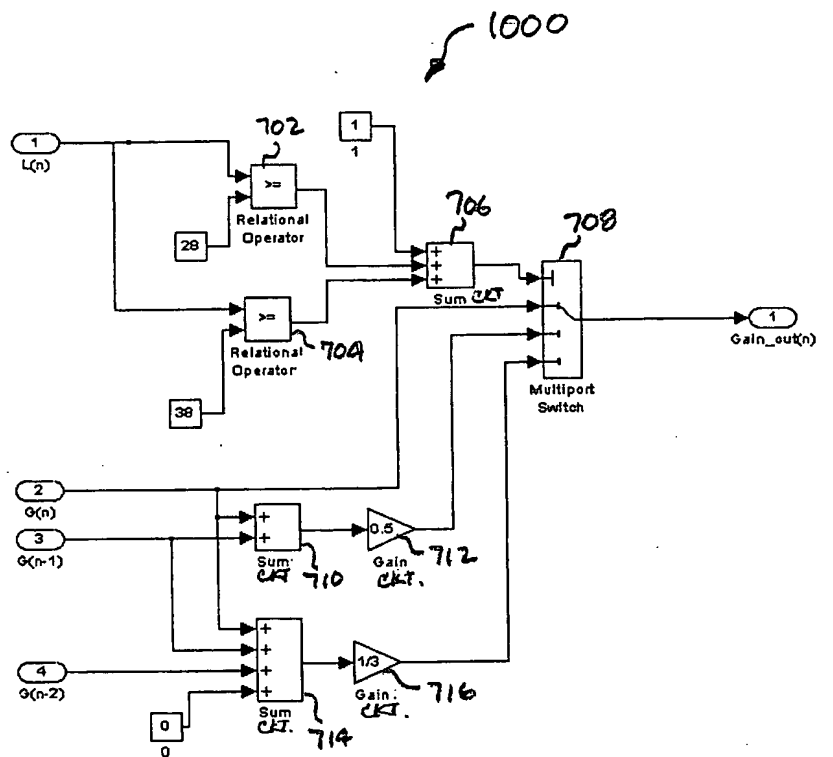


FIG. 10

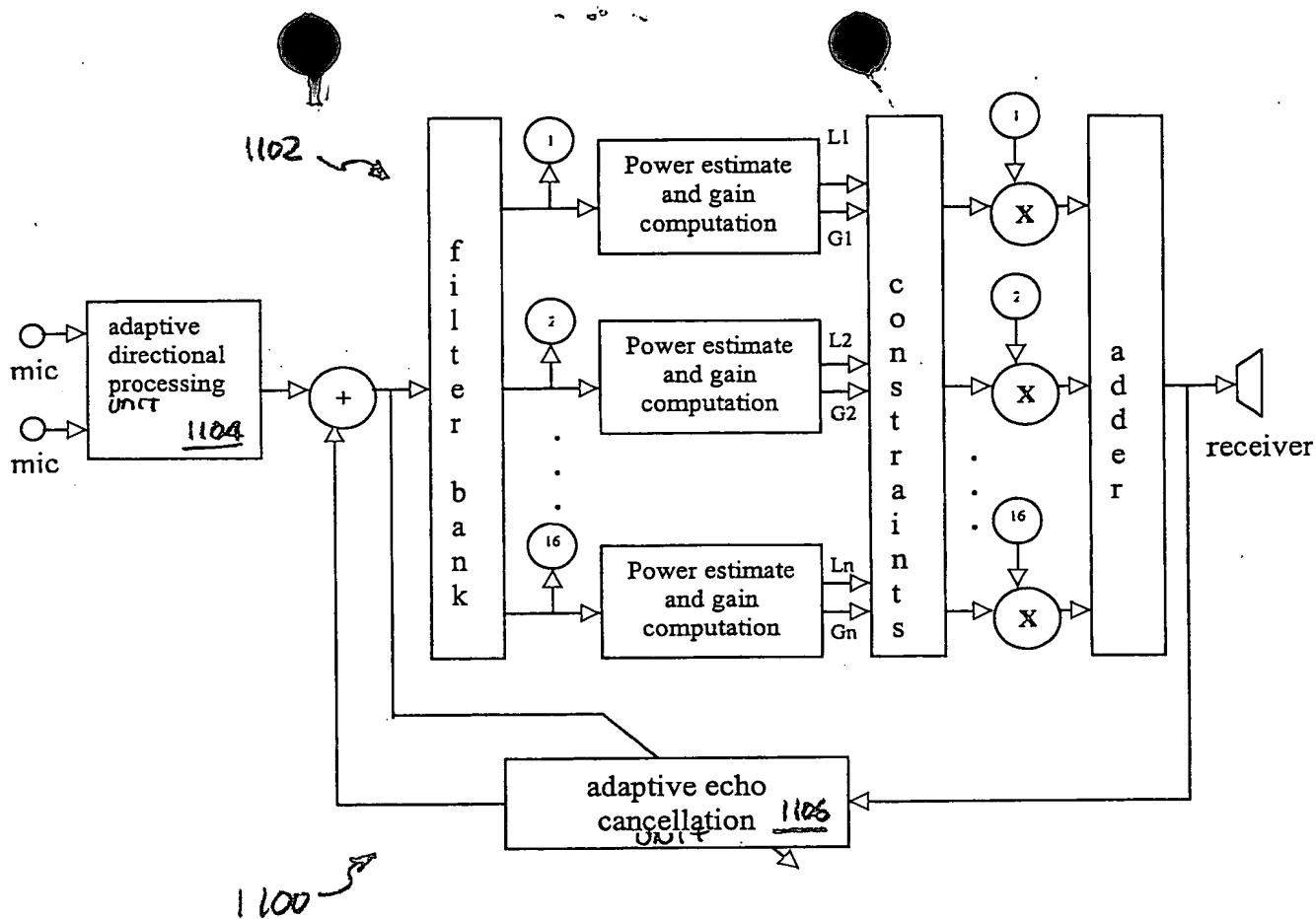


FIG. 11